

FIG. 1A

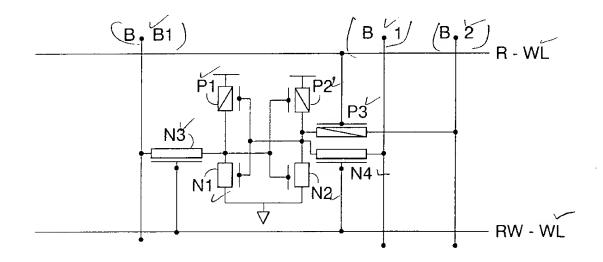
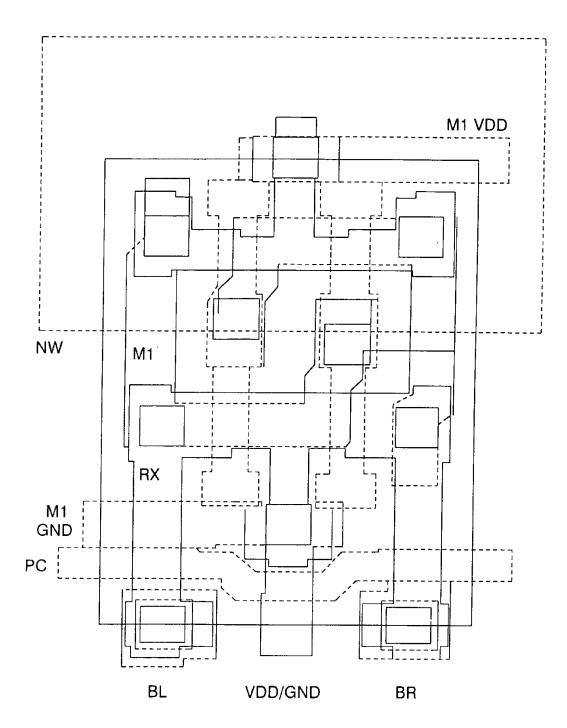


FIG. 1B



1 Port SRAM Cell 1.3 x 1.9 - 2.47

FIG. 1C(1) (Prior Art)

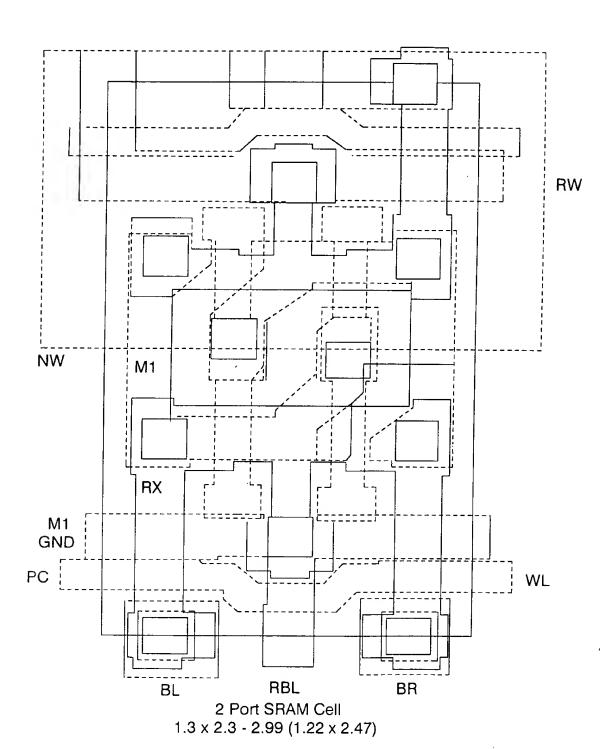
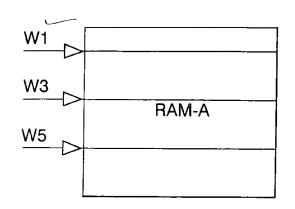
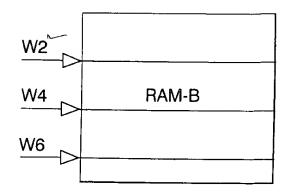
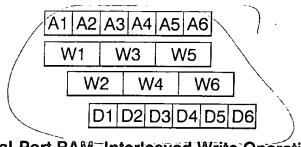


FIG. 1C(2)

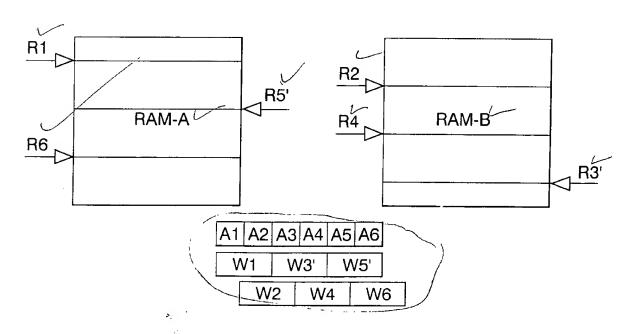






Dual-Port RAM, Interleaved Write Operation

FIG. 2



**Dual-Port RAM, Interleaved Write Operation** 

FIG. 3